



2871

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT: Joon-Hoo Choi et al.      Docket No.: 8071-12 (OPP 011059 US)  
Serial No.: 10/083,261      Group Art Unit: 2871  
Filed: February 25, 2002      Examiner: Wang, George Y  
  
For: THIN FILM TRANSISTOR ARRAY SUBSTRATE USING  
LOW DIELECTRIC INSULATING LAYER AND METHOD  
OF FABRICATING THE SAME

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO OFFICE ACTION**  
**SETTING FORTH A RESTRICTION REQUIREMENT**

Sir:

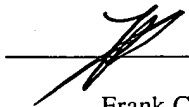
In response to the Office Action dated December 3, 2003, applicant provisionally elects claims 1, 2 and 6-12, with traverse. While there may be distinct species in claims 1-2 and 6-12 from claims 20 and 22-23 as set forth by the Examiner, applicant believes that examination of the two species will not present an undue burden.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Mail Stop Non-Fee Amendment, Commissioner for Patents, Alexandria, VA 22313-1450 on December 31, 2003.


Dated: 12/31/03

  
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Frank Chau

Indeed, examination of a reasonable number of species in the same application is encouraged. Under such circumstances, the Examiner is encouraged to maintain all claims in the same application. See, MPEP § 803.

Early and favorable consideration of this application are earnestly solicited.

Respectfully submitted,

By:   
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